DSP-CIS

Part-II : Filter Design & Implementation Chapter-6 : Filter Implementation

Marc Moonen

Dept. E.E./ESAT-STADIUS, KU Leuven marc.moonen@kuleuven.be www.esat.kuleuven.be/stadius/



Chapter-6 : Filter Implementation

- Introduction
 Filter implementation & finite wordlength problem
- Coefficient Quantization

Arithmetic Operations Quantization noise Statistical Analysis Limit Cycles Scaling

• **PS: Short version, does not include...** Fixed & floating point representations, overflow, etc. (see literature)

3/40





Filter implementation & finite word-length problem

- So far have assumed that signals/coefficients/arithmetic operations are represented/performed with <u>infinite</u> precision
- In practice, numbers are represented only to a <u>finite</u> precision, hence signals/coefficients/arithmetic operations are subject to quantization (truncation/rounding/...) errors
- Quantization effects relevant in <u>fixed-point</u> implementations with a `short' word-length (versus less of an issue when `sufficiently long' word-length is used (e.g. 24 bits), or with <u>floating-point</u> representations and arithmetic)
- Investigate impact of...
 - Quantization of filter coefficients
 - Quantization in arithmetic operations

DSP-CIS 2019-2020 / Chapter-6: Filter Implementation

5/40























Coefficient Quantization

Coefficient quantization effect on zero locations

 Analog filter design + bilinear transformation often lead to numerator polynomial of the form (e.g. 2nd-order cascade realization)

 $1 - 2\cos\theta_{i}z^{-1} + z^{-2}$ hence with zeros always on the unit circle

Quantization of the coefficient $2\cos\theta_i$ shifts zeros on the unit circle, which mostly has only minor effect on the filter characteristic. Hence mostly ignored...





Arithmetic Operations

Quantization noise problem

- If two B-bit numbers are added, the result is a B+1 bit number.
- If two B-bit numbers are multiplied, the result is a 2B-1 bit number.
- Typically (especially so in an IIR (feedback) filter), the result of an addition/multiplication has to be represented again as a B' -bit number (e.g. B' =B). Hence have to remove least significant bits (*).
- Rounding/truncation/... to B' bits introduces guantization noise.
- The effect of quantization noise is usually analyzed in a statistical manner (see p.20-25)
- Quantization, however, is a deterministic non-linear effect, which may give rise to limit cycle oscillations (see p.26-30)

(*) ..and/or most significant bits - not considered here

19/40















Quantization Noise / Limit Cycles

Example:	<pre>y[k] = -0.625.y[k-1]+u[k] 4-bit truncation (instead of rounding) input u[k]=0, y[0]=3/8 output y[k] = 3/8, -1/4, 1/8, 0, 0, 0, (no limit cycle!)</pre>	
Example:	y[k] = 0.625.y[k-1]+u[k] 4-bit rounding input u[k]=0, y[0]=3/8 output y[k] = 3/8, 1/4, 1/8, 1/8, 1/8, 1/8,	
Example: Conclusion	y[k] = 0.625.y[k-1]+u[k] 4-bit truncation input u[k]=0, y[0]=-3/8 output y[k] = -3/8, -1/4, -1/8, -1/8, -1/8, -1/8, on: weird, weird, weird, !	
DSP-CIS 2019-2020 / Chapter-6: Filter Implementation 27 / 4		27 / 40





29/40





















